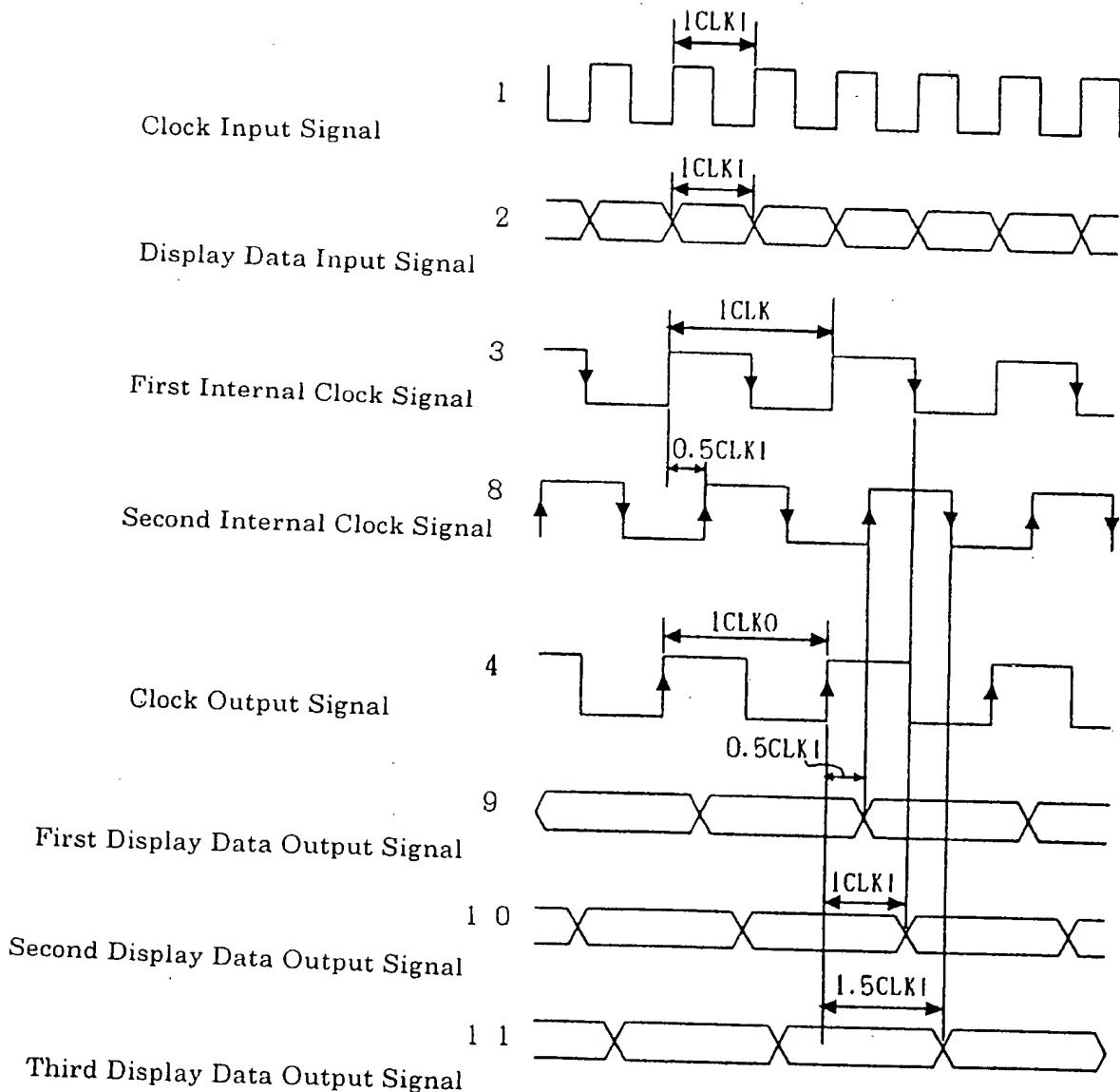




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filed 2/25/99
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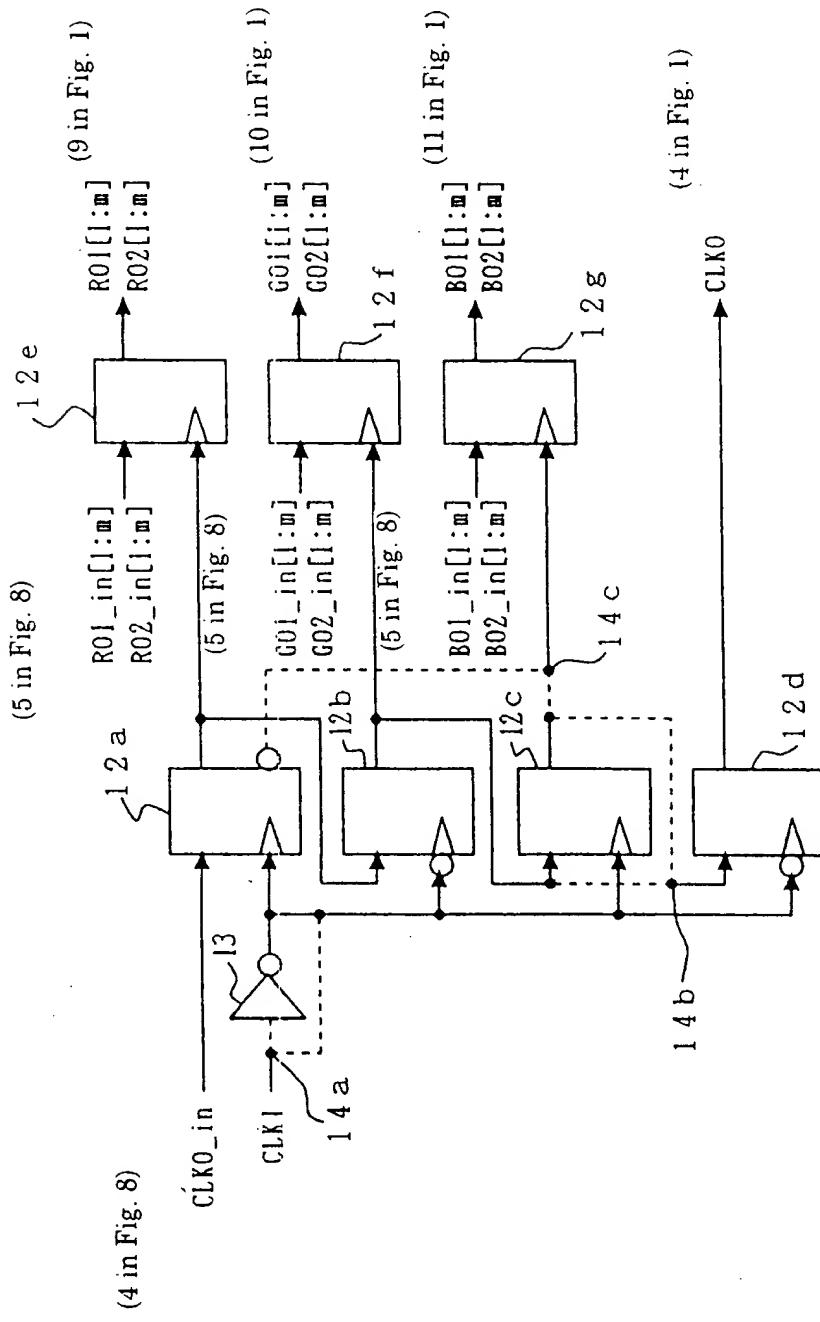
Fig 1





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Fig 2



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Fig 3

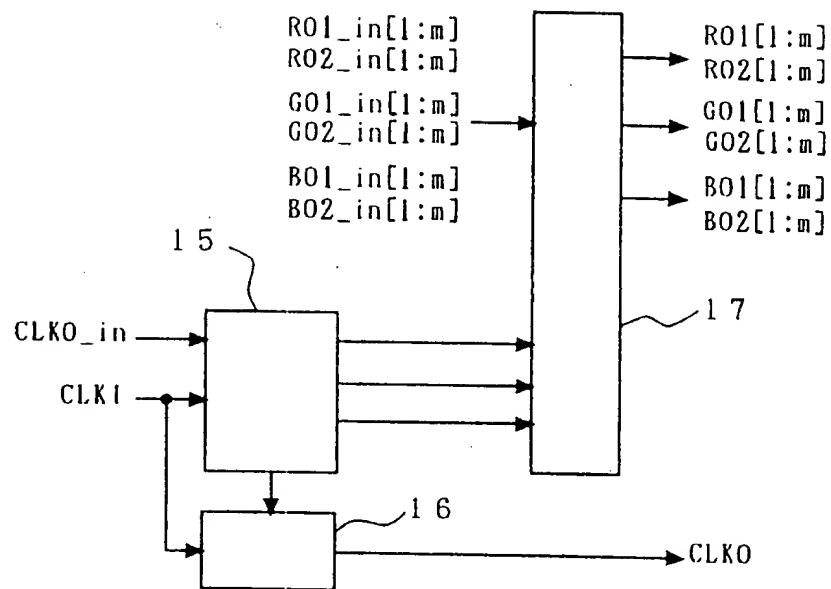
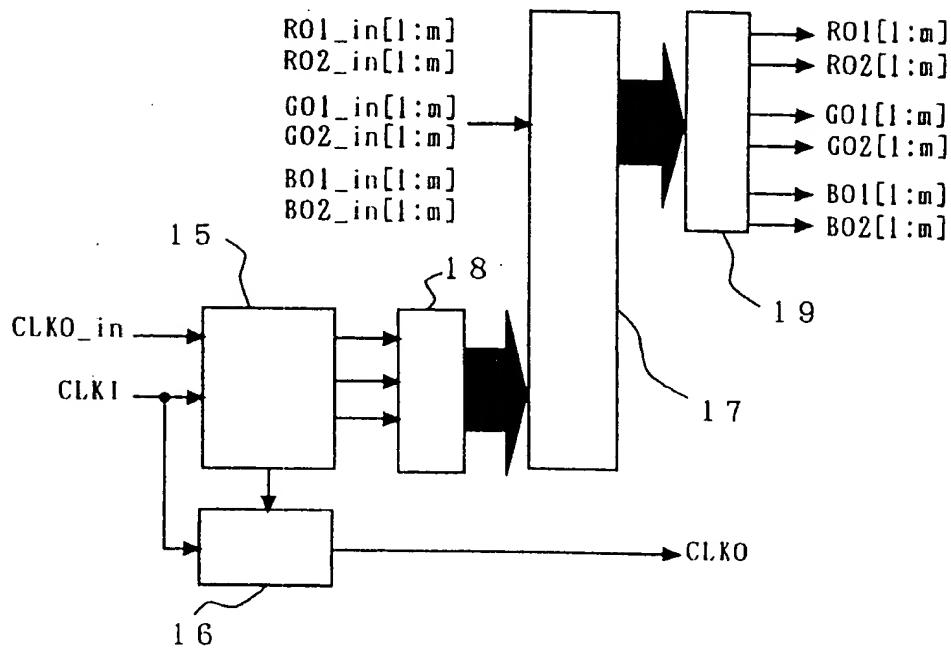


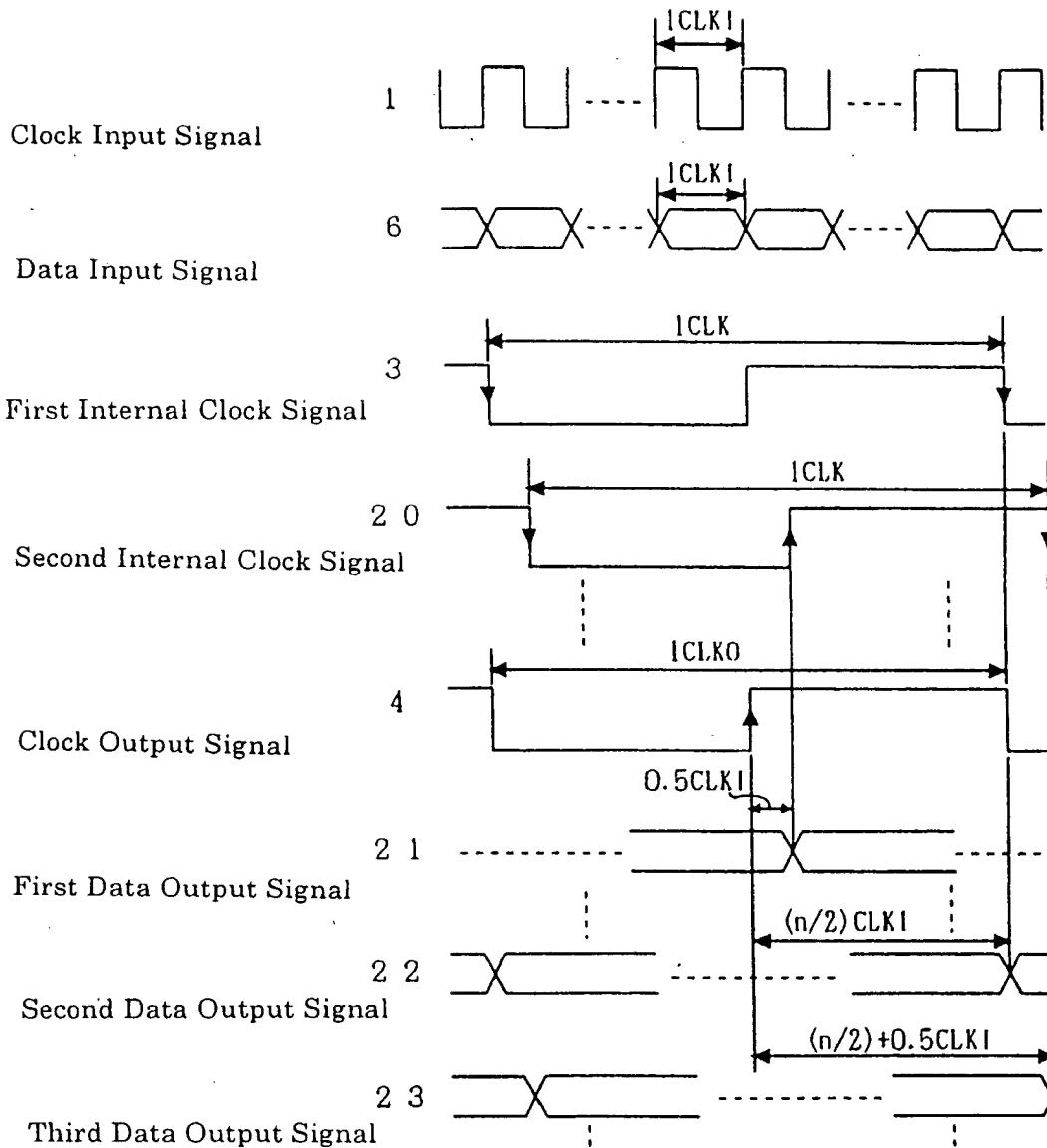
Fig 4



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Fig 5



REPLACEMENT SHEET



Fig 6

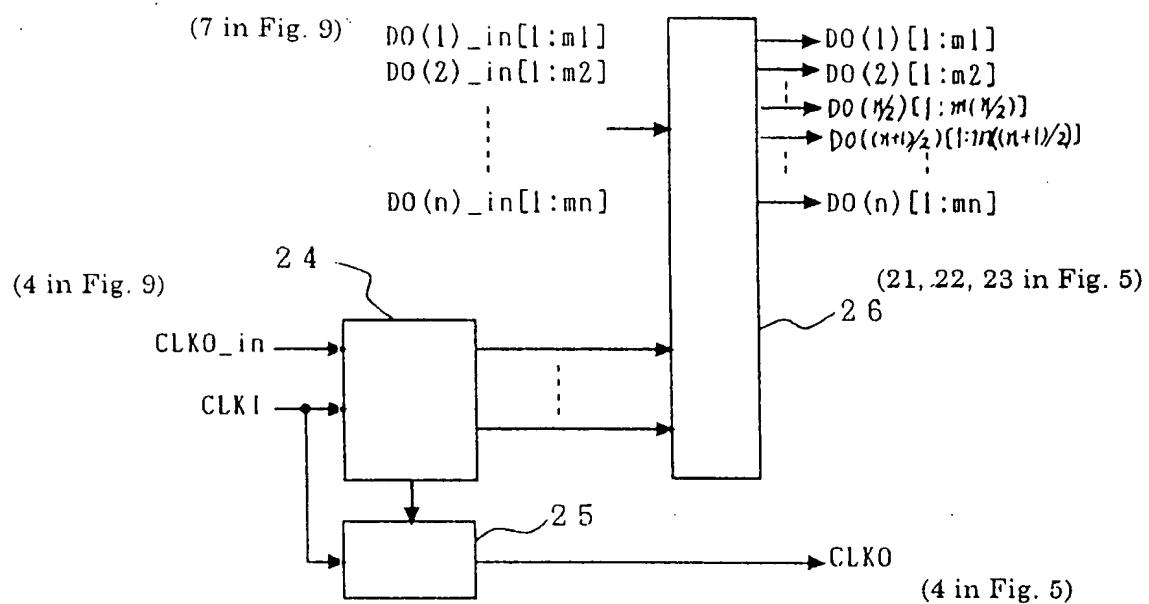
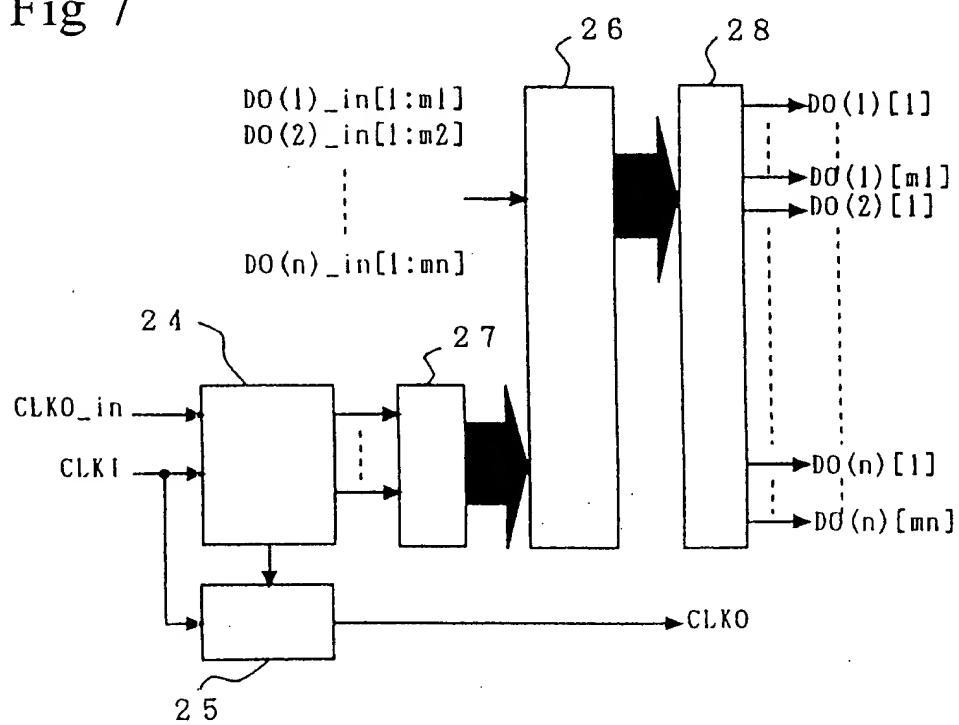


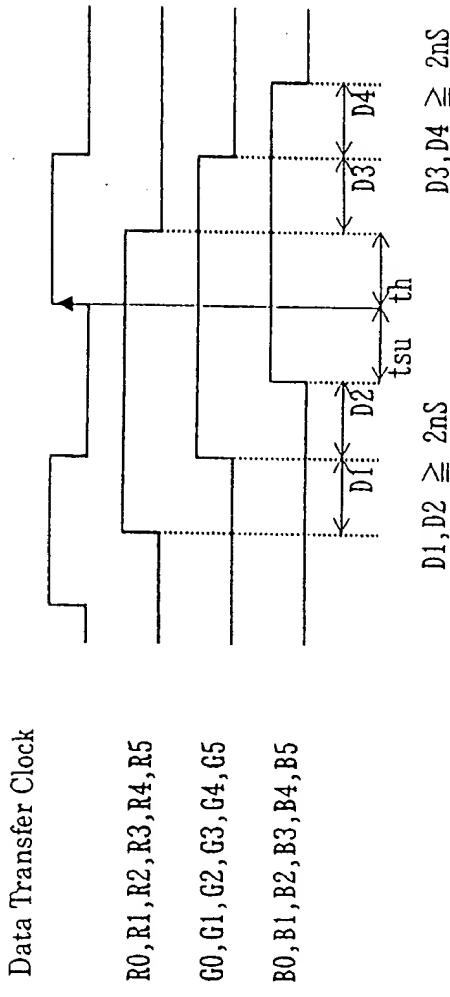
Fig 7



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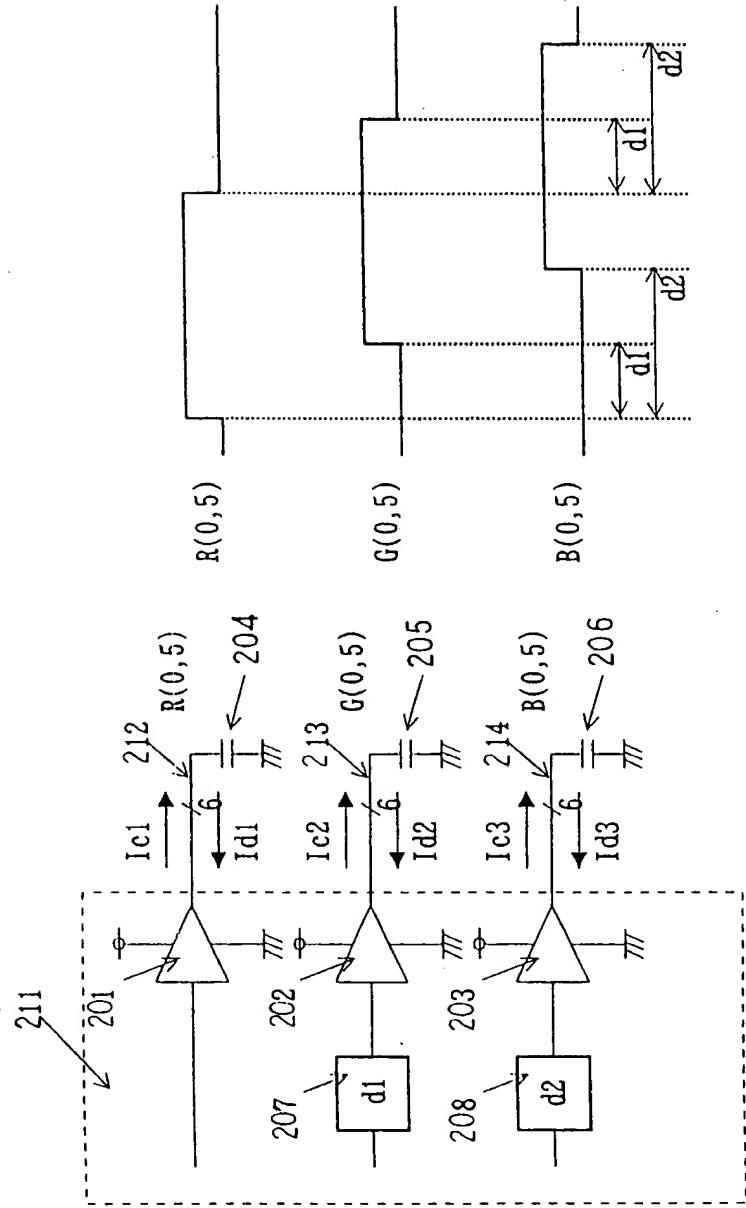
Fig 8



REPLACEMENT SHEET



Fig 9





REPLACEMENT SHEET

Fig 10

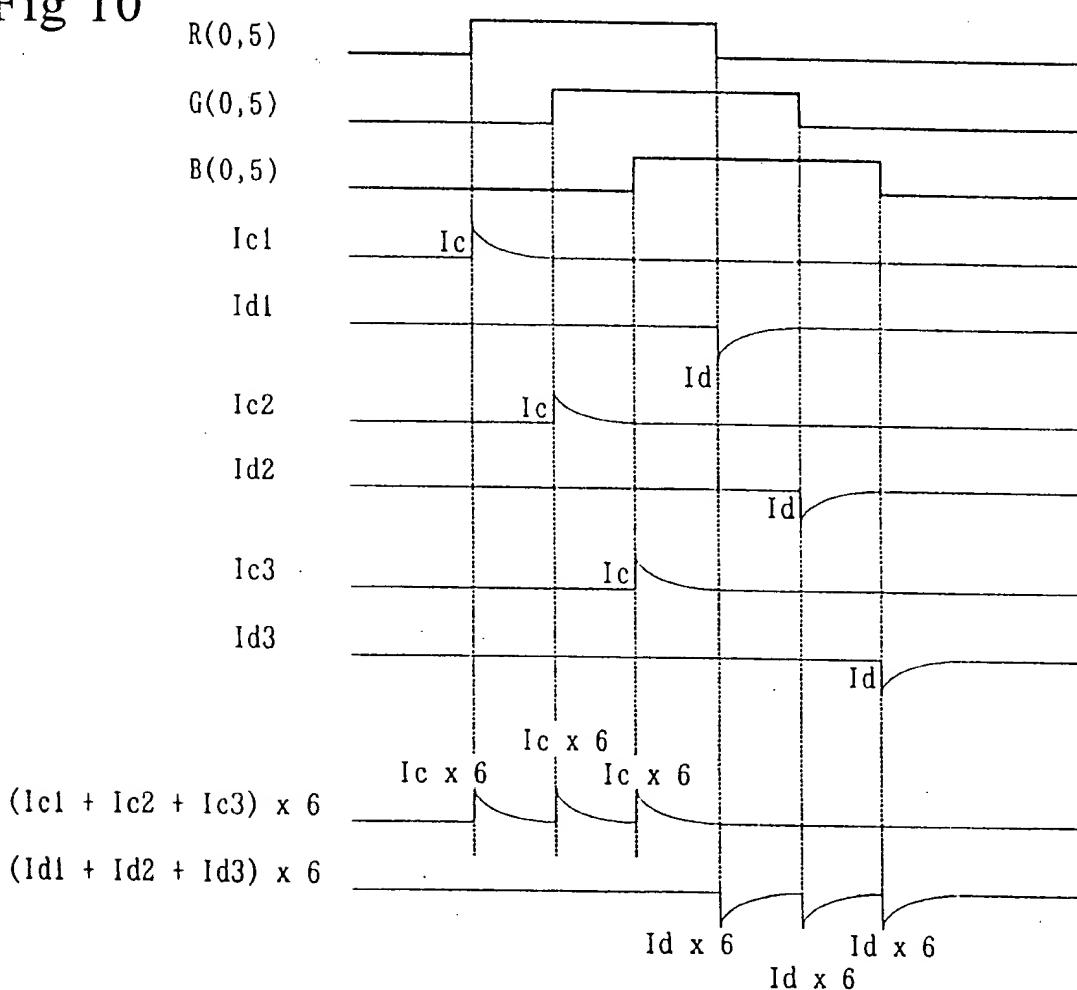


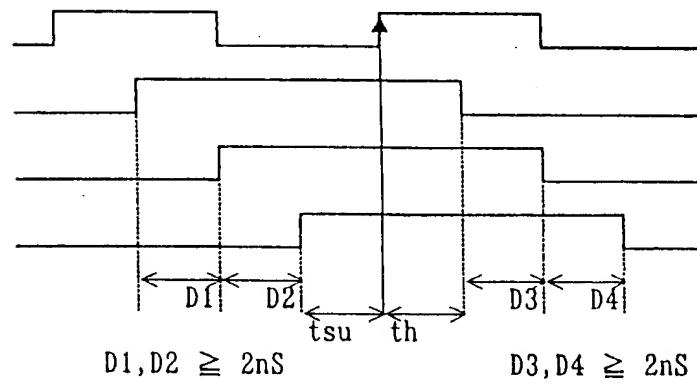
Fig 11

Data Transfer Clock

$R0, R1, G0, G1, B0, B1$

$R2, R3, G2, G3, B2, B3$

$R4, R5, G4, G5, B4, B5$



REPLACEMENT SHEET



Fig 12

	m	$m+1$	$m+2$	$m+3$	$m+4$	$m+5$	$m+6$	$m+7$	$m+8$	$m+9$	$m+10$	$m+11$	$m+12$	$m+13$	$m+14$	$m+15$
n	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB
$n+1$	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB
$n+2$	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB
$n+3$	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB
$n+4$	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB
$n+5$	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB
$n+6$	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB
$n+7$	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB
$n+8$	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB
$n+9$	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB
$n+10$	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB
$n+11$	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB	RGB

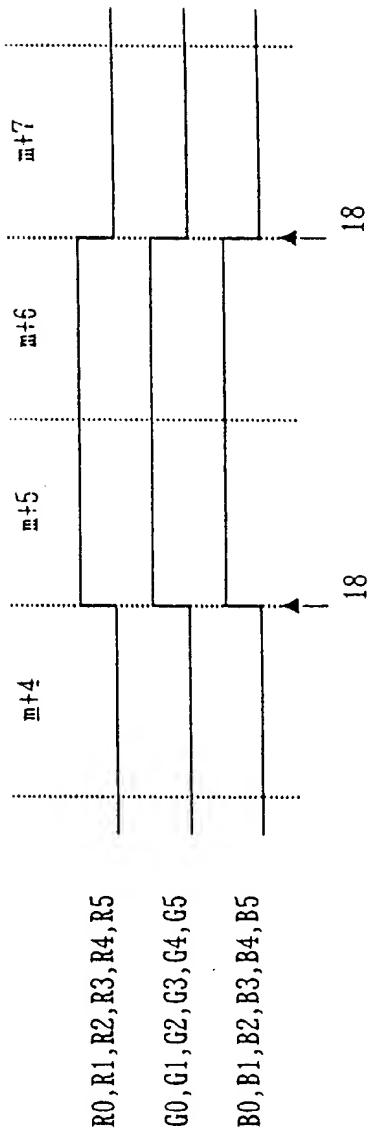
Fig 13

		$m+4$	$m+5$	$m+6$	$m+7$							
n	R	G	B	R	G	B	R	G	B	R	G	B
$n+1$	R	G	B	R	G	B	R	G	B	R	G	B
$n+2$	R	G	B	R	G	B	R	G	B	R	G	B
$n+3$	R	G	B	R	G	B	R	G	B	R	G	B

REPLACEMENT SHEET



Fig 14

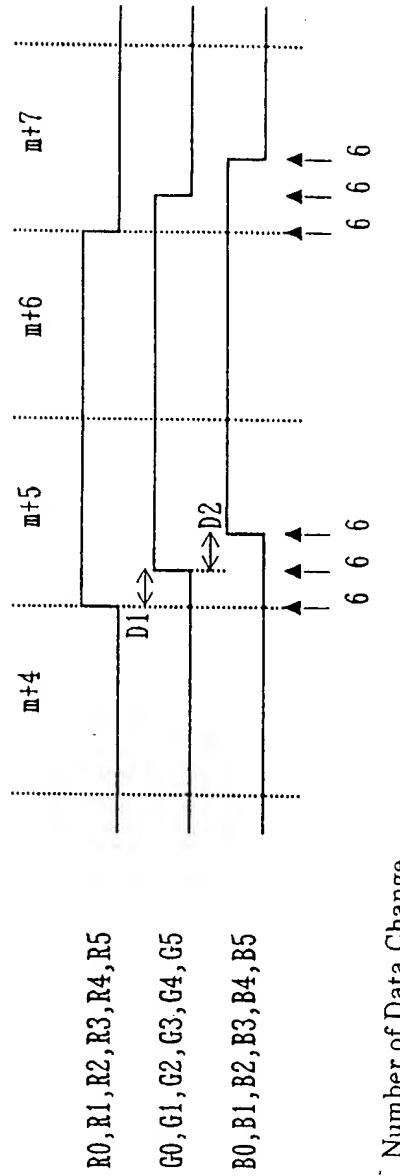


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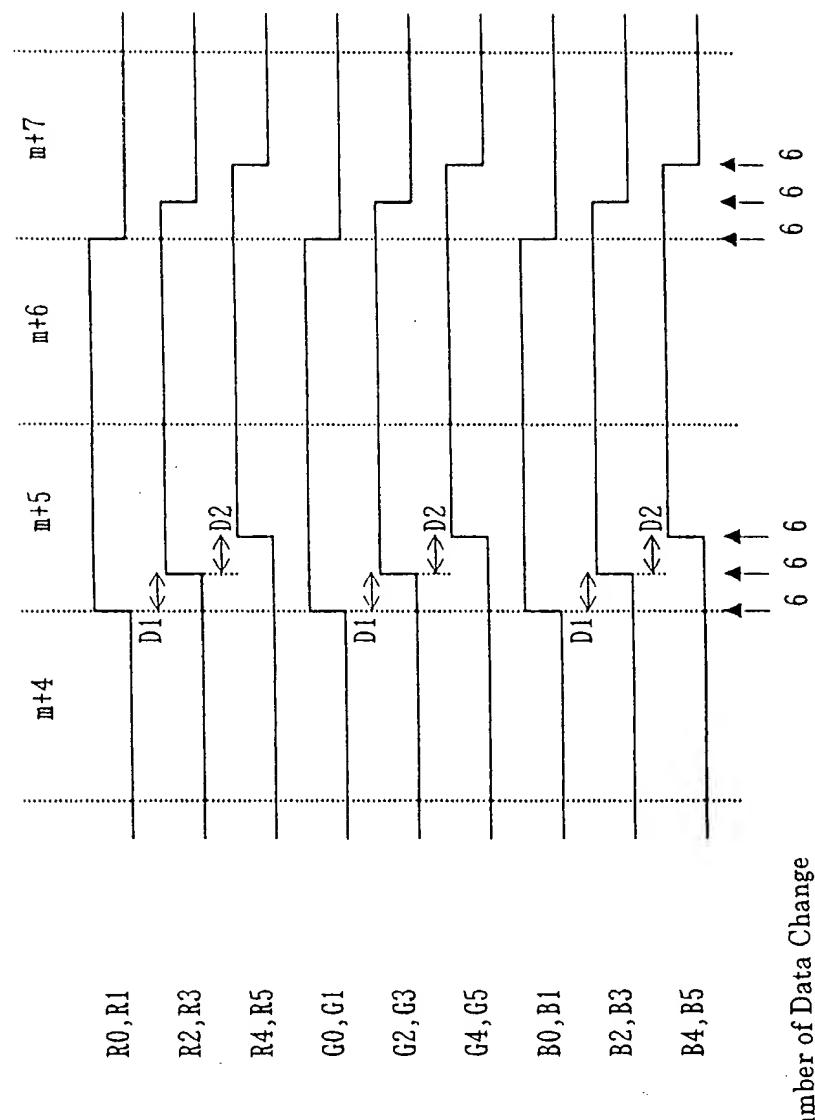
Fig 15



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Fig 16



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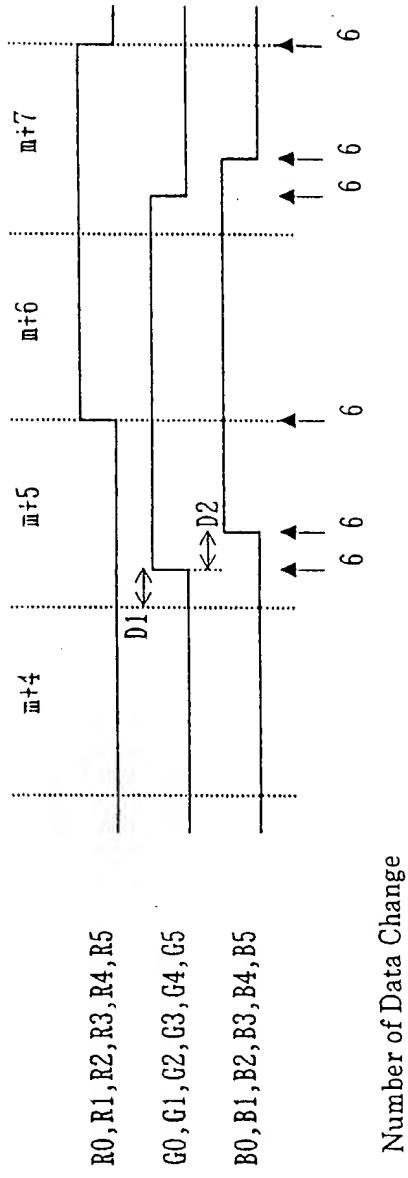
Fig 17

	m+4			m+5			m+6			m+7		
n	R	G	B	R	G	B	R	G	B	R	G	B
n+1	R	G	B	R	G	B	R	G	B	R	G	B
n+2	R	G	B	R	G	B	R	G	B	R	G	B
n+3	R	G	B	R	G	B	R	G	B	R	G	B

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Fig 18



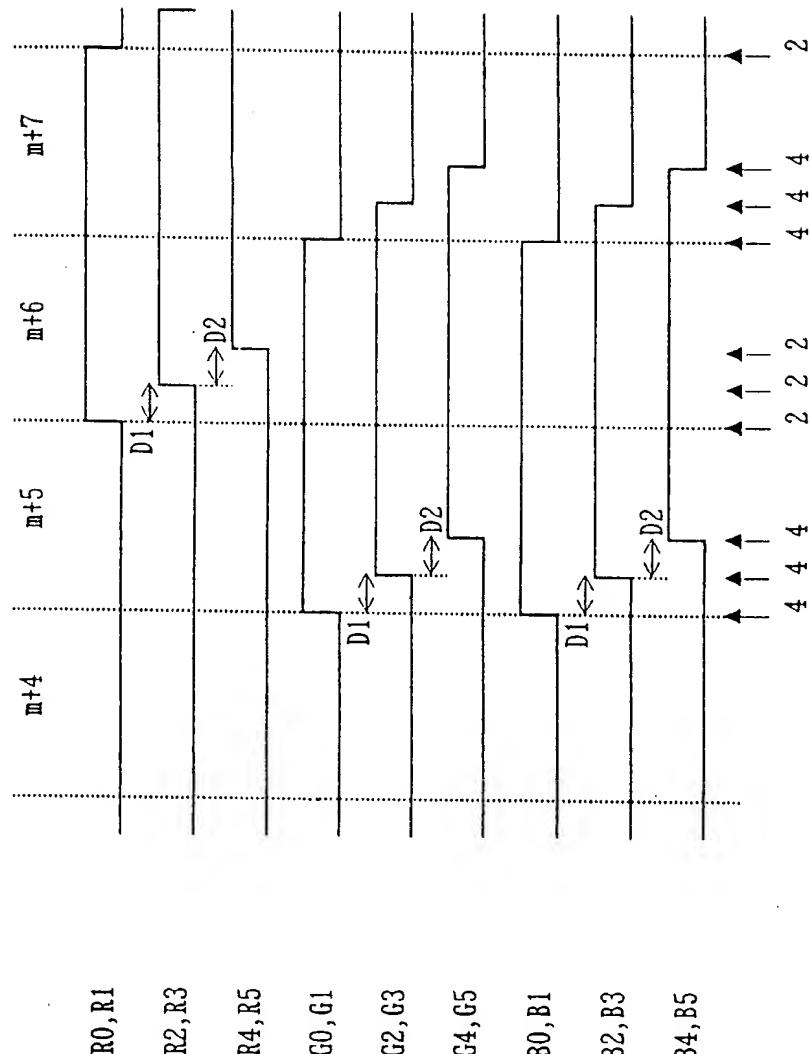
Number of Data Change

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Fig 19

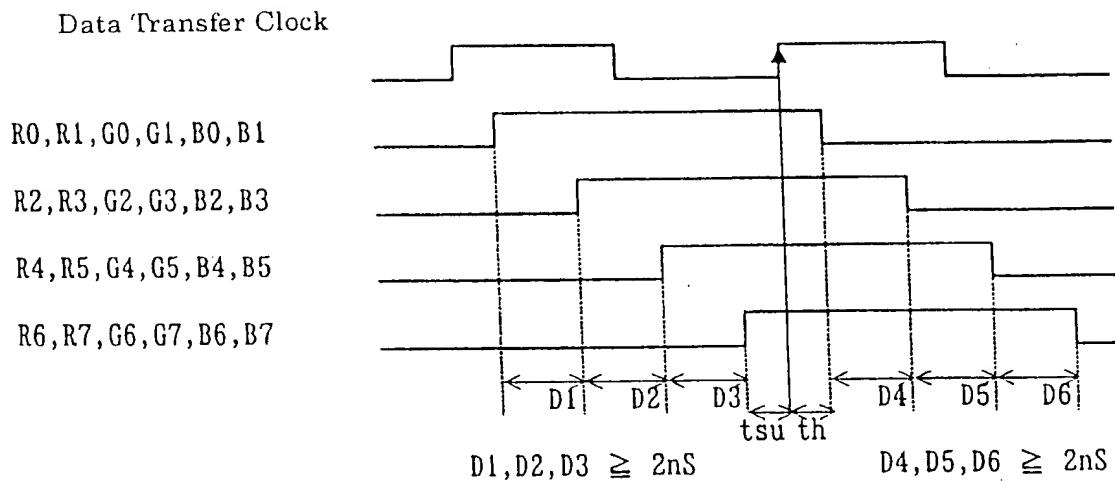


Number of Data Change

REPLACEMENT SHEET



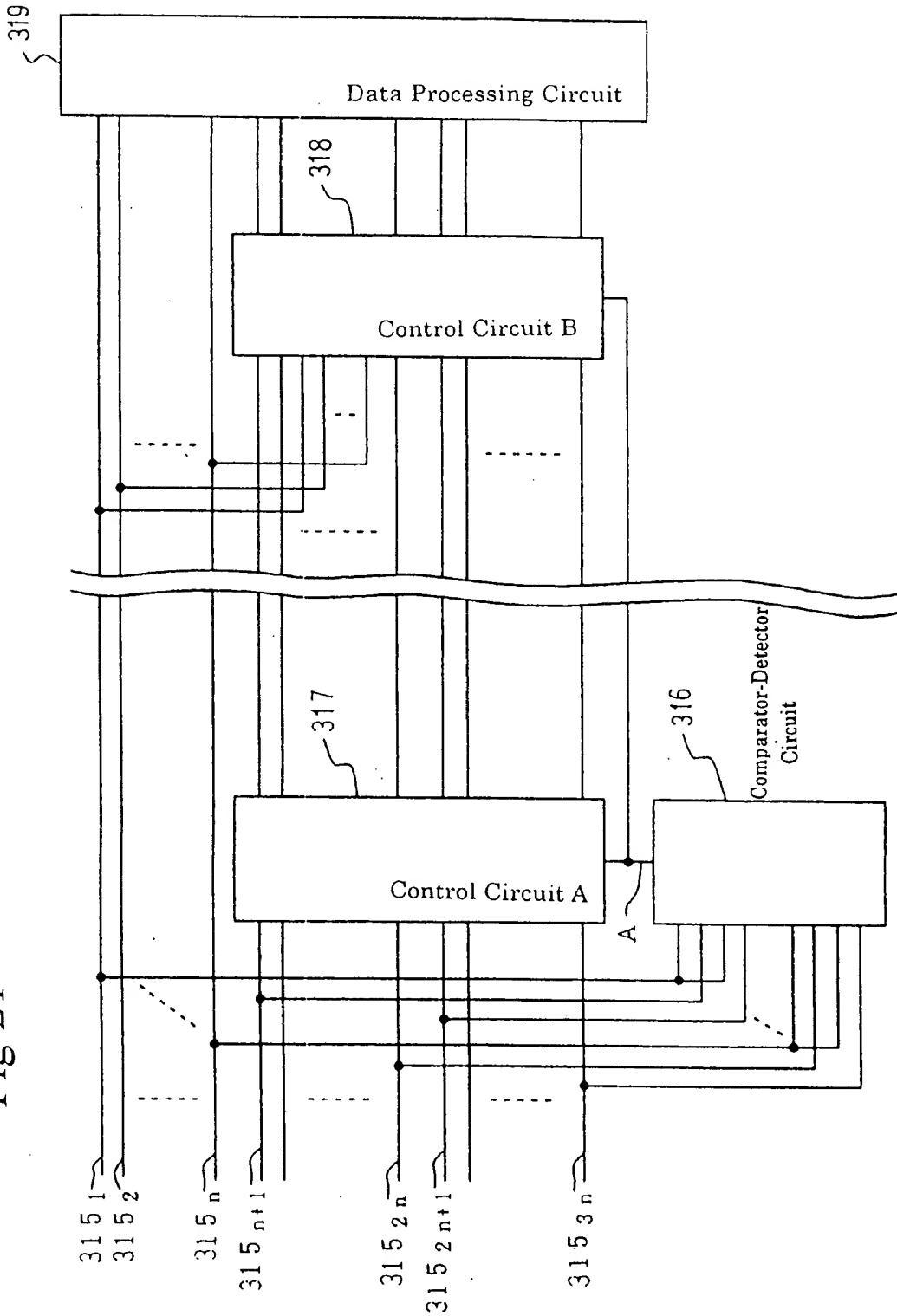
Fig 20



REPLACEMENT SHEET



Fig 21



Dedicated IC Side

Source Driver IC Side

REPLACEMENT SHEET



Fig 22

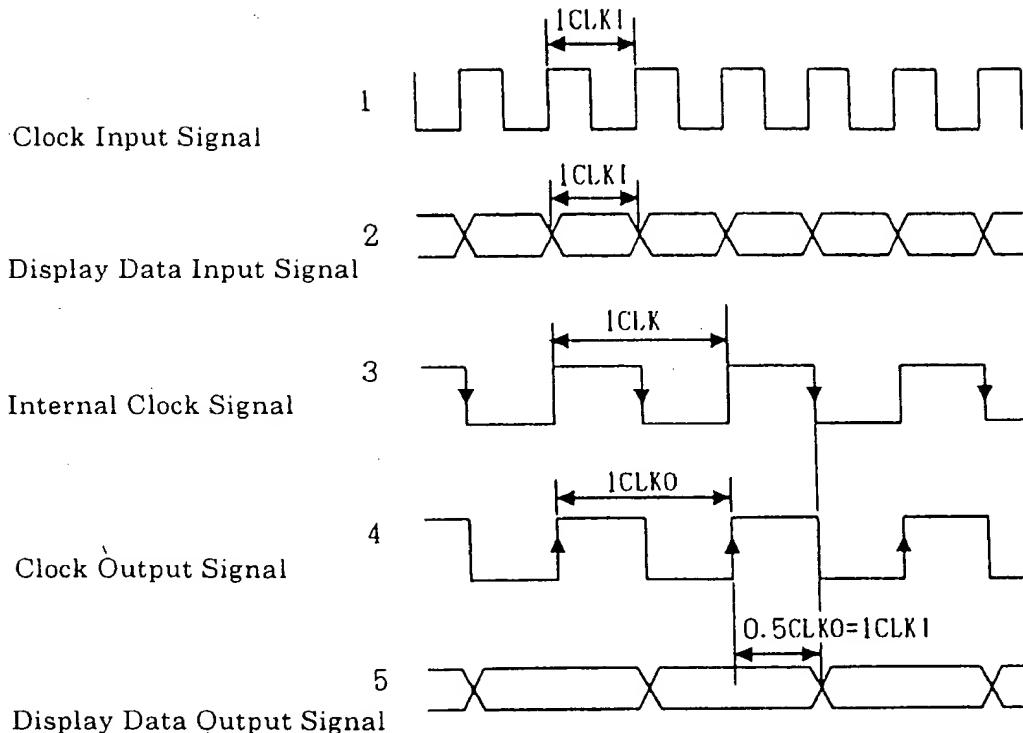
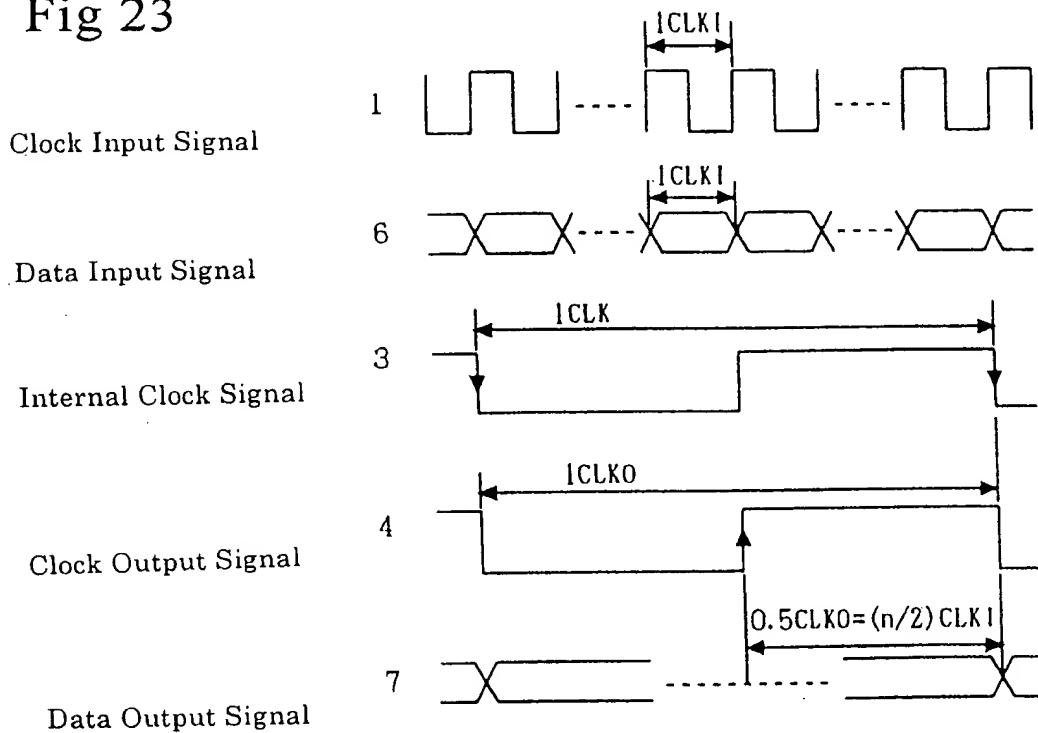


Fig 23



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Fig 24

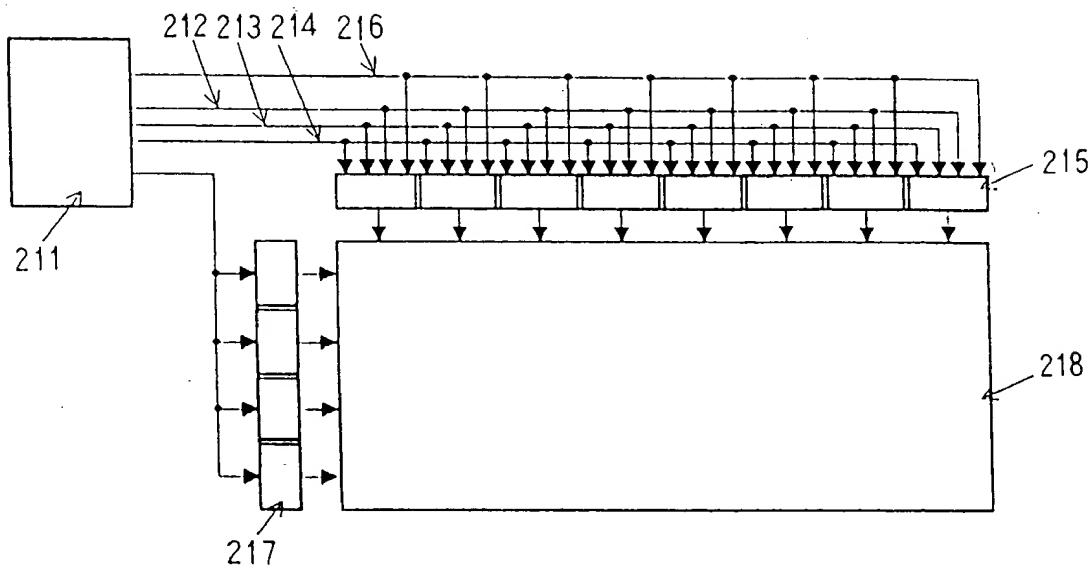
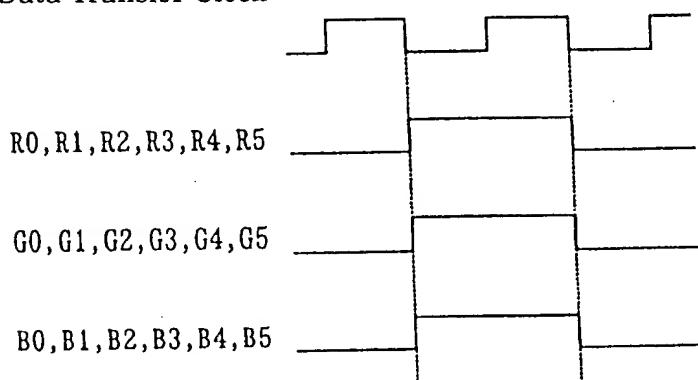


Fig 25

Data Transfer Clock



REPLACEMENT SHEET

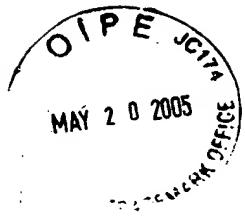
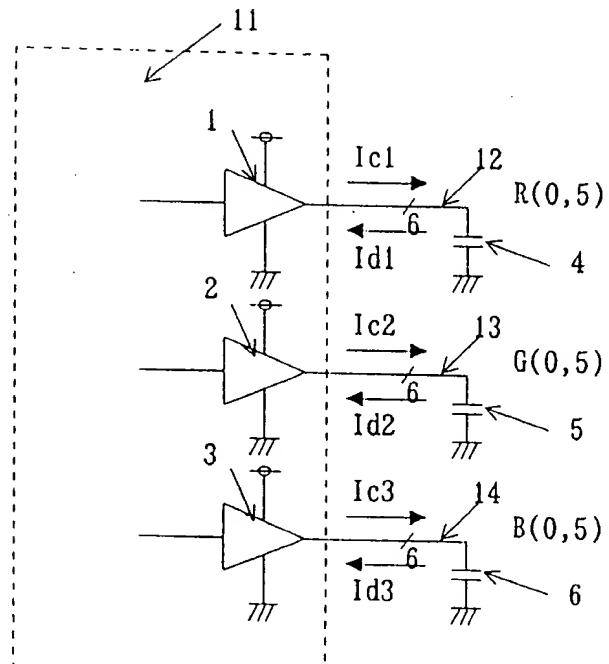


Fig 26



REPLACEMENT SHEET

Fig 27

Data Transfer Clock

$R(0,5), G(0,5), B(0,5)$

$Ic1$

$Id1$

$Ic2$

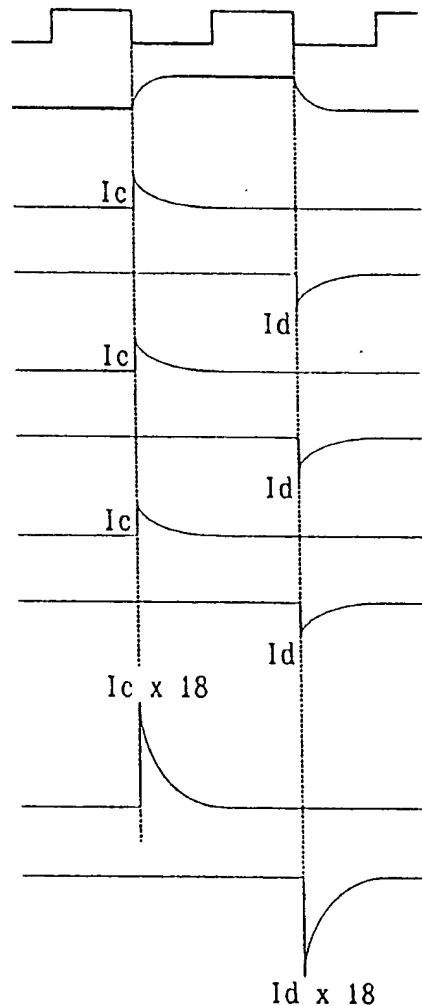
$Id2$

$Ic3$

$Id3$

$(Ic1 + Ic2 + Ic3) \times 6$

$(Id1 + Id2 + Id3) \times 6$



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Fig 28

